

## REMARKS

Responding to paragraphs 1 and 2 of the Office Action, the rejection of claims 1-2 and 4 under 35 U.S.C. 102(b) as being anticipated by Petrosino (U.S. Patent No. 5,712,822) is respectfully traversed. Currently amended claim 1 is limited to a line decoder for a memory cell comprising a synchronous portion..., an asynchronous portion... and a feedback-resetting portion... Petrosino does not teach or suggest any line decoder, much a line decoder of the type claimed. A decoder is mentioned only once by Petrosino (Col. 3, line 40). The Petrosino decoder is an instruction decoder, not a line decoder for a memory cell responsive to an address as claimed. Petrosino is not in the field of the claimed invention and is nonanalogous art. A decoder is shown in Fig. 3 in the upper left portion of the drawing, but Petrosino provides no teaching about its structure or operation, other than the reference in Col. 3, line 40. There is no teaching or suggestion that the decoder shown in Fig. 3 has any of the limitations of claim 1, much less all of them as would be required for a 102(b) anticipation. As a result, claim 1 is in condition for allowance.

Claims 2 and 4 are dependent on claim 1 and are allowable for the same reasons as claim 1.

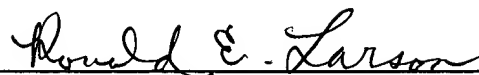
Responding to paragraph 3 of the Office Action, the Examiner's statement that claims 6-10 and 12-16 are allowable is gratefully acknowledged.

Claims 6, 8-10, 12, and 14-16 have been amended to correct matters of form. None of claims 6, 8-10, 12 and 14-16 has been narrowed.

In summary, each of claims 1, 2, 4, 6-10 and 12-16 is allowable and such action is respectfully solicited.

Date: October 21, 2003

Respectfully submitted,

A handwritten signature in cursive script, reading "Ronald E. Larson", positioned above a horizontal line.

Ronald E. Larson  
Reg. No. 24,478  
Attorney for Applicant

McAndrews, Held & Malloy, Ltd.  
500 W. Madison, 34<sup>th</sup> Floor  
Chicago, IL 60661  
312.775.8000